09/459703

CofC

Applicant: Kiran A. Padwekar	Examiner: Meonske, Tonia
Patent No.: 7,100,027	Group Art Unit: 2181
Issue Date: August 29, 2006	Docket No: 884.027US1

Title: SYSTEM AND METHOD FOR REPRODUCING SYSTEM EXECUTIONS USING A REPLAY

HANDLER (As Amended)

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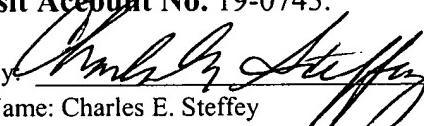Alexandria, VA 22313-1450
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Richard Beck

Name

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IN UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.:	7,100,027	Docket No:	884.027US1
Issue Date:	August 29, 2006	Patentee:	Kiran A. Padwekar
Customer No.:	21186	Confirmation No.:	1539
Title	SYSTEM AND METHOD FOR REPRODUCING SYSTEM EXECUTIONS USING A REPLAY HANDLER (As Amended)		

REQUEST FOR CERTIFICATE OF CORRECTION

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It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. A copy of the text of the Certificate in the suggested form are enclosed.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.
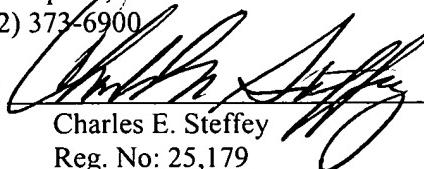
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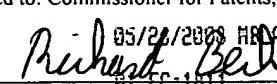
Respectfully Submitted
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CERTIFICATE OF CORRECTION

PATENT NO   : 7,100,027   Page (1) of 1

DATED   : August 29, 2006

INVENTOR(S)   : Padwekar

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the face page, in field (75), in "Inventor", in column 1, line 1, delete "Santa Clara" and insert - - San Jose - -, therefor.

On the face page, in field (56), under "Other Publications", in column 2, line 1, after "L." insert - - , - -.

On the face page, in field (56), under "Other Publications", in column 2, lines 4-5, delete "techdd. htm" and insert - - techdd.htm - -, therefor.

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US Serial No.: 09/459,703　　**US Patent No.: US 7,100,027 B1**　　**Issue Dt.: Aug. 29, 2006**

Title: SYSTEM AND METHOD FOR REPRODUCING SYSTEM EXECUTIONS USING A REPLAY HANDLER

PR Instructions: Face Page, Claims and Abstract

Sr. No.	P/S	Original		Issued Patent		Description of Error
		Page	Line	Column	Line	
1	P	Page 2 of 3 Oath or Declaration filed (04/10/2000)	1 (Inventor's Residence)	First Page Col. 1 (Inventor)	1	Delete "Santa Clara" and insert - - San Jose - -, therefor.
2	P	Page 1 of 1 List of references cited by examiner (06/30/2004)	Entry 1 Line 1 (Non-Patent Documents)	First Page Col. 2 (Other Publications)	1	After "L." insert - - , - -.
3	P	Page 1 of 1 List of references cited by examiner (01/12/2005)	Entry 1 Line 1 (Non-Patent Documents)	First Page Col. 2 (Other Publications)	4-5	Delete "techdd. htm" and insert - - techdd.htm - -, therefor.

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(54) **SYSTEM AND METHOD FOR REPRODUCING SYSTEM EXECUTIONS USING A REPLAY HANDLER**

(75) Inventor: **Kiran A. Padwekar,** Santa Clara, CA (US)

(73) Assignee: **Intel Corporation,** Santa Clara, CA (US)

(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.

(21) Appl. No.: **09/459,703**

(22) Filed: **Dec. 13, 1999**

(51) **Int. Cl.**
　G06F 11/30　　(2006.01)
　G06F 11/36　　(2006.01)

(52) **U.S. Cl.** 712/227; 712/228; 714/17; 714/25; 717/127

(58) **Field of Classification Search** 712/227, 712/228; 714/17, 25, 30; 717/127–135
See application file for complete search history.

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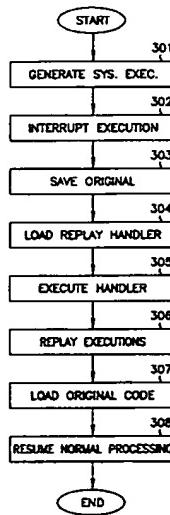
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Primary Examiner—Fritz Fleming
Assistant Examiner—Tonia L. Meonske
(74) *Attorney, Agent, or Firm*—Schwegman, Lundberg, Woessner & Kluth, P.A.

(57) **ABSTRACT**

Methods and systems for replaying arbitrary system executions are disclosed. A system includes a storage element, a memory hierarchy and a processor. The memory hierarchy is coupled to the storage element. The processor is coupled to the memory hierarchy. The processor executes instructions from the memory hierarchy. A replay handler is loaded into the memory hierarchy. The replay handler is executed for replaying at least one execution. In another embodiment, a method for replaying executions is disclosed. Normal execution of a processor is interrupted. A replay/restart kernel is loaded. At least one execution is replayed. Normal execution of the processor is resumed.

18 Claims, 4 Drawing Sheets

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